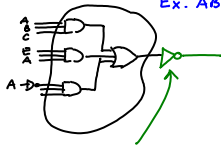


Have direct relationship with SOP expression
Ex. $AB+CD$

Caution:
"AND-OR" config. / "AND-OR" circuit
This has special meaning.
This is not simply circuit with AND/OR/Inverter.



If an inverter is added after the OR gate, then the circuit is called

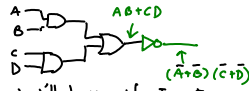
AND-OR-Invert circuit.

Have direct relationship with POS expression.

There is sth more about this config.
"AND-OR" circuit is of the following form:

Ex. SOP: $AB+CD$

AND-OR config. circuit



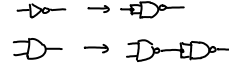
Now, what'll happen if I put one more inverter at the output?

$$\begin{aligned} \text{Output: } & \overline{AB+CD} \\ & = \overline{AB} \cdot \overline{CD} \\ & = (\overline{A+B}) \cdot (\overline{C+D}) \leftarrow \text{POS} \end{aligned}$$

NAND Circuit:

There are two ways to transform circuits into NAND circuit

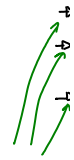
① From any circuits,



(change other gates to their NAND equivalent constructions)

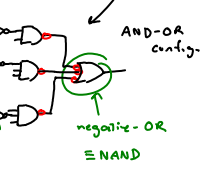
② From any circuits / expressions, convert them into SOP expressions.

Easy: into NAND



Convert: to NAND by $\overline{[]}$

to turn SOP expression
AND circuit.



inverters
AND gates

